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APPLICATION NO. FILING DATE FIRST NAMED INVENTOR ATTORNEY DOCKET NO. CONFIRMATION NO. Masato Sumikawa 0033-0694P 2465 09/782,180 02/14/2001 2292 7590 08/05/2002 BIRCH STEWART KOLASCH & BIRCH **EXAMINER** PO BOX 747 NGUYEN, DILINH P FALLS CHURCH, VA 22040-0747 ART UNIT PAPER NUMBER 2814

Please find below and/or attached an Office communication concerning this application or proceeding.

			on No.	Applicant(s)	
, Office Action Summary		09/782,18	09/782,180 SUMIKAWA ET AL.		
		Examiner		Art Unit	
		DiLinh Ng	uyen	2814	·
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply					
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). - Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b). Status					
1)[Responsive to communication(s) filed on 15 May 2002.				
2a)⊠	This action is FINAL . 2b) This	is action is non-final.			
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213. Disposition of Claims					
4)⊠ Claim(s) <u>1-18</u> is/are pending in the application.					
4a) Of the above claim(s) is/are withdrawn from consideration.					
_	Claim(s) is/are allowed.				
	Claim(s) <u>1-18</u> is/are rejected.				
	Claim(s) is/are objected to.				
8) Claim(s) are subject to restriction and/or election requirement. Application Papers					
9) The specification is objected to by the Examiner.					
10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.					
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).					
11) The proposed drawing correction filed on is: a) approved b) disapproved by the Examiner.					
If approved, corrected drawings are required in reply to this Office action.					
12) The oath or declaration is objected to by the Examiner.					
Priority under 35 U.S.C. §§ 119 and 120					
13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).					
a)⊠ All b)□ Some * c)□ None of:					
1. Certified copies of the priority documents have been received.					
2. Certified copies of the priority documents have been received in Application No					
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.					
14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).					
a) The translation of the foreign language provisional application has been received. 15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.					
Attachment(s)					
2) Notic	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449) Paper No(s) <u>8 a</u>		· _	(PTO-413) Paper No(s) atent Application (PTO-	

U.S. Patent and Trademark Office PTO-326 (Rev. 04-01)

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DETAILED ACTION

Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 1-2, 4 and 17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Toyosawa (U.S. Pat. 6337257) in view of Tao (U.S. Pat. 6410981).
 - Regarding claims 1 and 17, Toyosawa discloses a semiconductor device (figs. 2-3B, column 7, lines 30 et seq.) comprising:

a semiconductor wafer where semiconductor elements are formed (abstract) having a surface 34 provided with an external connection electrode 47;

a back surface 36 opposite that with the external connection electrode, wherein grinding scratches formed by the grinding are removed to smooth the back surface (abstract); and

the back surface 36 of the chips 32 are in contact with another protective tape (column 12, lines 29-31). It would have been obvious that the protective tape reinforced the back surface of the chip.

Regarding claims 2 and 4, Toyosawa doesn't specifically point out that the protective tape is formed of epoxy resin. It would have been obvious matter of design choice to provide the protective tape is formed of epoxy resin. For

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instance, Ohuchi (U.S. Pat. 6271588) discloses a protective tape is polymide or an epoxy resin (column 2, lines 46-50).

3. Claim 3 is rejected under 35 U.S.C. 103(a) as being unpatentable over Toyosawa (U.S. Pat. 6337257) in view of Horiuchi et al. (U.S. Pat. 6242799).

Toyosawa discloses the claimed invention except for the resin is formed of a material having an elastic modulus of $1.5 \times 10^6 \text{ N/m}^2$ to $5.0 \times 10^6 \text{ N/m}^2$. Horiuchi et al. disclose a resin having a Young's modulus of 10 MPa or less (column 5, lines 50-55). Horiuchi et al. do not specifically point out the range of the Young's modulus is from 1.5 $\times 10^6 \text{ N/m}^2$ to $5.0 \times 10^6 \text{ N/m}^2$, but 10 MPa or less is included in the range. It would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the device of Toyosawa to provide a resin are suitably used, as shown by Horiuchi et al.

- 4. Claims 5-8 and 18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Tamaki et al. (U.S. Pat. 6136668).
 - Regarding claims 5 and 18, Tamaki discloses a method of manufacturing a semiconductor device comprising the steps of:

grinding and polishing processes are performed for the back surface of the semiconductor wafer 4a; having an external connection electrode on the opposite surface; and applying adhesive material 31 on the grinded surface (figs. 7-10, column 7, lines 1-6). It would have been obvious that the adhesive material is formed of resin.

 Regarding claim 6, Tamaki discloses the step of cutting the semiconductor substrate after the step of applying (column 7, lines 43-45 and 55-60).

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 Regarding claims 7-8, Tamaki discloses the step of previously grinding the surface to be abrased.

4. Claims 9-12 are rejected under 35 U.S.C. 103(a) as being unpatentable over Tamaki et al. (U.S. Pat. 6136668) in view of Sakaguchi et al. (U.S. Pat. 6150194).

Tamaki et al. disclose the claimed invention except for the step of the resin is printed. Sakaguchi et al. disclose a method for producing a semiconductor device, comprising the step of: resin is printed (column 4, lines 60-64) to provide better control of thickness of the resin layer on a tape substrate. Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the device of Tamaki et al. to control the appropriate thickness of the resin layer for the device, as shown by Sakaguchi et al.

5. Claims 13-16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Tamaki et al. in view of Takahashi et al. (U.S. Pat. 6153448).

Tamaki et al. disclose the claimed invention except for the step of the resin is applied by spin-coating. Takahashi et al. disclose a method for manufacturing a semiconductor device comprising the step of:

an insulating resin is spin-coated on the semiconductor wafer (column 8, lines 51-54) to adjust the thickness of the resin layer. Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the device of Tamaki et al. to adjust the thickness of the resin layer for the device, as shown by Takahashi et al.

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Response to Arguments

Applicant's arguments with respect to claims 1-16 have been considered but are most in view of the new ground(s) of rejection. Therefore, see the new rejection above.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DiLinh Nguyen whose telephone number is (703) 305-6983. The examiner can normally be reached on 8:00AM - 6:00PM (M-F).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, OLIK CHAUDHURI can be reached on (703) 306-2794. The fax phone numbers for the organization where this application or proceeding is assigned are (703)

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~ 308-7722 for regular communications and (703) 308-7724 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

DLN July 20, 2002

> OLIK CHAUDHURI SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800